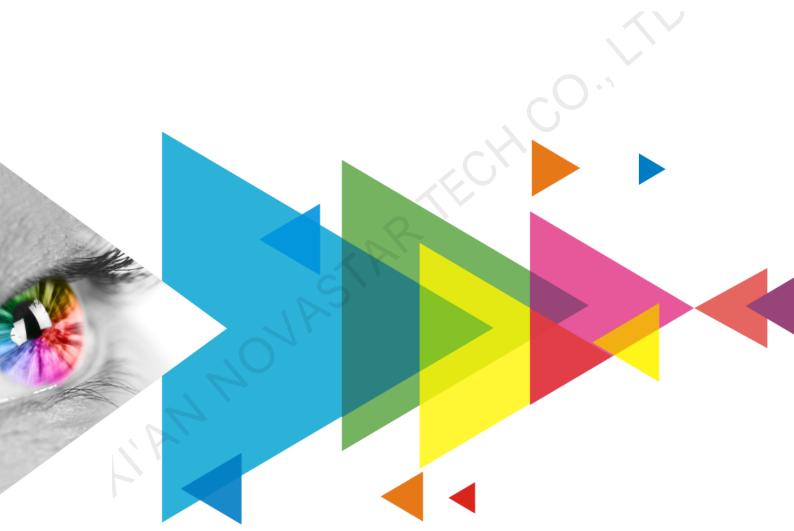


# A9s

# **Receiving Card**

V1.1.1



**Specifications** 

# **Change History**

| Document<br>Version | Firmware<br>Version | Release Date | Description  |
|---------------------|---------------------|--------------|--|
| V1.1.1              | V4.5.3.0            | 2020-07-01   | <ul> <li>Updated the firmware version.</li> <li>Updated the indicator description.</li> <li>Updated the side dimensions diagram.</li> <li>Optimized the feature description.</li> <li>Optimized the pin description tables.</li> </ul> |
| V1.1.0              | N/A                 | 2018-09-14   | Optimized the quick seam correction function.  |
| V1.0.0              | N/A                 | 2017-01-10   | First release  |

### Introduction

The A9s is a high-end small receiving card developed by NovaStar. A single A9s loads up to 512×512 pixels (PWM IC). Supporting the pixel level brightness and chroma calibration, and 3D functions, the A9s can greatly improve the display effect and user experience.

The A9s uses high-density connectors for communication to limit the effects of dust and vibration, resulting in high stability. It supports up to 32 groups of parallel RGB data or 64 groups of serial data (expandable to 128 groups of serial data). Its reserved pins allow for custom functions of users. Thanks to its EMC Class B compliant hardware design, the A9s has improved electromagnetic compatibility and is suitable to various on-site setups.

### **Features**

### **Improvements to Display Effect**

- Pixel level brightness and chroma calibration
  Working with NovaLCT and NovaCLB, the
  receiving card supports brightness and chroma
  calibration on each LED, which can effectively
  remove color discrepancies and greatly improve
  LED display brightness and chroma consistency,
  allowing for better image quality.
- Quick adjustment of dark or bright lines
   The dark or bright lines caused by splicing of
   cabinets or modules can be adjusted to improve
   the visual experience. This function is easy to
   use and the adjustment takes effect immediately.
- 3D function

- Working with the independent controller which supports 3D function, the receiving card supports 3D image output.
- Image rotation in 90° increments
   The display image can be set to rotate in multiples of 90° (0°/ 90°/180°/270°).

### **Improvements to Maintainability**

Smart module (supported by dedicated firmware)
Working with the smart module, the receiving
card supports module ID management, storage
of calibration coefficients and module
parameters, monitoring of module temperature,
voltage and flat cable communication status,
LED error detection, and recording of the
module run time.

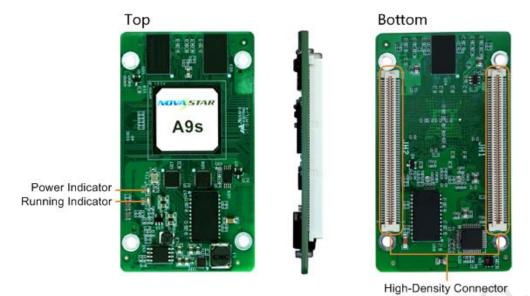
- Automatic module calibration
   After a new module with flash memory is installed to replace the old one, the calibration coefficients stored in the flash memory can be automatically uploaded to the receiving card when it is powered on.
- Module Flash management
   For modules with flash memory, the information stored in the memory can be managed. The calibration coefficients and module ID can be stored and read back.
- One click to apply calibration coefficients stored in module Flash
   For modules with flash memory, if the Ethernet cable is disconnected, users can hold down the self-test button on the cabinet to upload the calibration coefficients in the flash memory of the module to the receiving card.
- Mapping function
   The cabinets display the receiving card number and Ethernet port information, allowing users to easily obtain the locations and connection topology of receiving cards.
- Setting of a pre-stored image in receiving card
  The image displayed on the screen during
  startup, or displayed when the Ethernet cable is
  disconnected or there is no video signal can be
  customized.
- Temperature and voltage monitoring
   The temperature and voltage of the receiving
   card can be monitored without using peripherals.
- Cabinet LCD
   The LCD module connected to the cabinet can display the temperature, voltage, single run time and total run time of the receiving card.
- Firmware program readback
   The firmware program of the receiving card can be read back and saved to the local computer.
  - NovaLCT V5.2.0 or later is required.
- Configuration parameter readback
   The configuration parameters of the receiving card can be read back and saved to the local computer.
- LVDS transmission (supported by dedicated firmware)
   Low-voltage differential signaling (LVDS)
   transmission is used to reduce the number of

data cables from the hub board to module, increase the transmission distance, and improve the signal transmission quality and electromagnetic compatibility (EMC).

### **Improvements to Reliability**

- Dual card backup and status monitoring
  In an application with requirements for high
  reliability, two receiving cards can be mounted
  onto a single hub board for backup. In the case
  that the main receiving card fails, the backup
  card will serve to ensure uninterrupted operation
  of the display.
  - The working status of the main and backup receiving cards can be monitored in NovaLCT V5.2.0 or later.
- Status detection of dual power supplies
   When two power supplies are connected, their
   working status can be detected by the receiving
   card.
- Loop backup
   The receiving cards and the sending card form a
   loop via the main and backup line connections. If
   a fault occurs at a location of the lines, the
   screen can still display the image normally.
- Dual backup of configuration parameters
   The receiving card configuration parameters are
   stored in the application area and factory area of
   the receiving card at the same time. Users
   usually use the configuration parameters in the
   application area. If necessary, users can restore
   the configuration parameters in the factory area
   to the application area.
- Dual backup of the application program
   Two copies of the application program are stored
   in the receiving card at the factory to avoid the
   problem that the receiving card may get stuck
   due to program update exception.
- Dual backup of calibration coefficients
   The calibration coefficients are stored in the
   application area and factory area of the receiving
   card at the same time. Users usually use the
   calibration coefficients in the application area. If
   necessary, users can restore the calibration
   coefficients in the factory area to the application
   area.

## **Appearance**



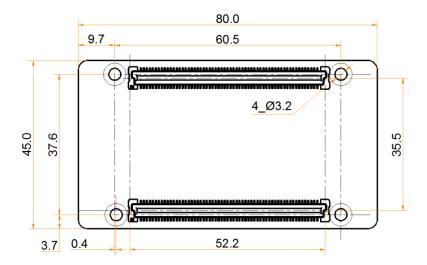
All product pictures shown in this document are for illustration purpose only. Actual product may vary.

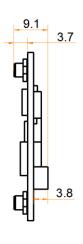
## **Indicators**

| Indicator         | Color | Status                      | Description   |
|-------------------|-------|-----------------------------|---|
| Running indicator | Green | Flashing once every 1s      | The receiving card is functioning normally. Ethernet cable connection is normal, and video source input is available. |
|                   |       | Flashing once every 3s      | Ethernet cable connection is abnormal.  |
|                   |       | Flashing 3 times every 0.5s | Ethernet cable connection is normal, but no video source input is available.  |
|                   |       | Flashing once every 0.2s    | The receiving card failed to load the program in the application area and now is using the backup program.            |
|                   |       | Flashing 8 times every 0.5s | A redundancy switchover occurred on the Ethernet port and the loop backup has taken effect.                           |
| Power indicator   | Red   | Always on                   | The power input is normal.  |

# **Dimensions**

The board thickness is not greater than 2.0 mm, and the total thickness (board thickness + thickness of components on the top and bottom sides) is not greater than 9.5 mm. Ground connection (GND) is enabled for mounting holes.

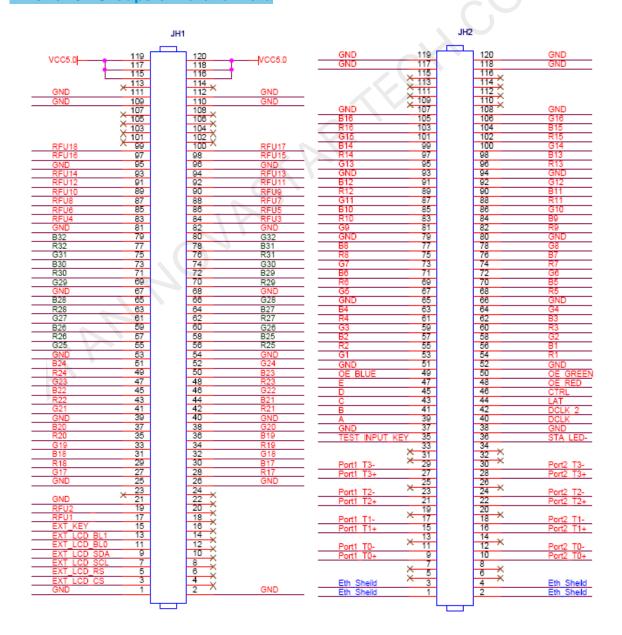




Tolerance: ±0.1 Unit: mm

## **Pins**

### Pins for 32 Groups of Parallel Data

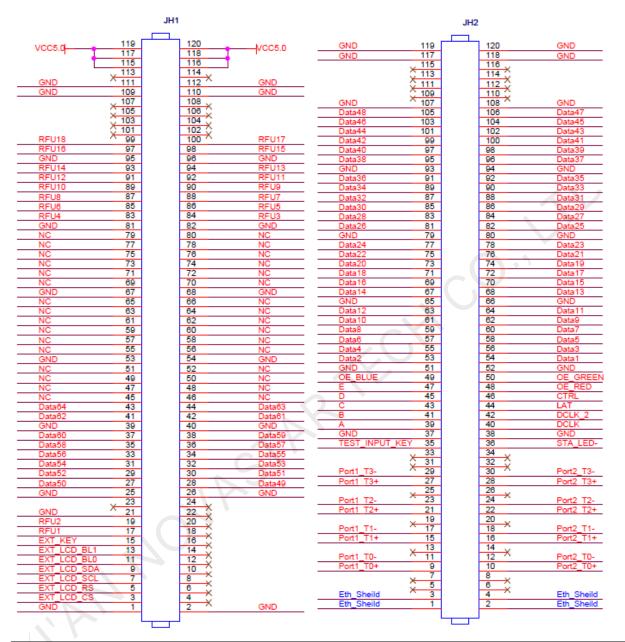


|                        |             | JH        | 1        |            |            |
|------------------------|-------------|-----------|----------|------------|------------|
|                        | GND         | 1         | 2        | GND        |            |
| LCD CS signal          | EXT_LCD_CS  | 3         | 4        | NC         |            |
| LCD RS signal          | EXT_LCD_RS  | 5         | 6        | NC NC      |            |
| LCD clock signal       | EXT_LCD_SCL | 7         | 8        | NC NC      |            |
| LCD data signal        | EXT_LCD_SDA | 9         | 10       | NC NC      |            |
| LCD backlight signal 1 | EXT_LCD_BL0 | 11        | 12       | NC NC      |            |
| LCD backlight signal 2 | EXT_LCD_BL1 | 13        | 14       | NC NC      |            |
| LCD control button     | EXT_KEY     | 15        | 16       | NC<br>NC   |            |
| /                      | RFU1        | 17        | 18       | NC NC      |            |
| 1                      | RFU2        | 19        | 20       | NC<br>NC   |            |
| ,                      | GND         | 21        | 22       | NC NC      |            |
|                        | NC          | 23        | 24       | NC<br>NC   |            |
|                        | GND         | 25        | 26       | GND        |            |
| 1                      | G17         | 27        | 28       | R17        | 1          |
| 1                      | R18         | 29        | 30       | B17        | <i>I</i>   |
| 1                      | B18         | 31        | 32       | G18        |            |
| · .                    |             |           |          |            | , i        |
| 1                      | G19<br>R20  | 33<br>35  | 34<br>36 | R19<br>B19 | 1          |
| 1                      |             |           |          |            | 1          |
| 1                      | B20         | 37        | 38       | G20        | ▼ <i>1</i> |
|                        | GND<br>C24  | 39        | 40       | GND        | 1          |
| 1                      | G21         | 41        | 42       | R21        | 1          |
| 1                      | R22         | 43        | 44       | B21        | /          |
|                        | B22         | 45        | 46       | G22        | /          |
| 1                      | G23         | 47        | 48       | R23        | /          |
| 1                      | R24         | 49        | 50       | B23        | 1          |
| /                      | B24         | 51        | 52       | G24        | 1          |
|                        | GND         | 53        | 54       | GND        |            |
|                        | G25         | 55        | 56       | R25        | /          |
| 1                      | R26         | 57        | 58       | B25        | /          |
| 1                      | B26         | 59        | 60       | G26        | /          |
| 1                      | G27         | 61        | 62       | R27        | /          |
| 1                      | R28         | 63        | 64       | B27        | 1          |
| /                      | B28         | <b>65</b> | 66       | G28        | 1          |
|                        | GND         | 67        | 68       | GND        |            |
| /                      | G29         | 69        | 70       | R29        | 1          |
| 1                      | R30         | 71        | 72       | B29        | 1          |
| 1                      | B30         | 73        | 74       | G30        | 1          |
| 1                      | G31         | 75        | 76       | R31        | /          |
| 1                      | R32         | 77        | 78       | B31        | /          |
| 1                      | B32         | 79        | 80       | G32        | /          |
|                        | GND         | 81        | 82       | GND        |            |
| 1                      | RFU4        | 83        | 84       | RFU3       | 1          |
| I                      | RFU6        | 85        | 86       | RFU5       | 1          |
| 1                      | RFU8        | 87        | 88       | RFU7       | 1          |
| 1                      | RFU10       | 89        | 90       | RFU9       | 1          |
| 1                      | RFU12       | 91        | 92       | RFU11      | 1          |
| 1                      | RFU14       | 93        | 94       | RFU13      | 1          |
|                        | GND         | 95        | 96       | GND        |            |
| 1                      | RFU16       | 97        | 98       | RFU15      | /          |
| 1                      | RFU18       | 99        | 100      | RFU17      | /          |
|                        | NC          | 101       | 102      | NC         |            |
|                        | NC          | 103       | 104      | NC         |            |
|                        | NC          | 105       | 106      | NC         |            |
|                        | NC          | 107       | 108      | NC         |            |
|                        | GND         | 109       | 110      | GND        |            |
|                        | GND         | 111       | 112      | GND        |            |
|                        | NC          | 113       | 114      | NC         |            |
|                        | VCC         | 115       | 116      | VCC        |            |
|                        | VCC         | 117       | 118      | VCC        |            |
|                        | VCC         | 119       | 120      | VCC        |            |
|                        | V 00        | 113       | 120      | VOO        |            |

|                       |                |     | 10             |            |                          |
|-----------------------|----------------|-----|----------------|------------|--------------------------|
| Changin around        | Eth Chaild     |     | -12<br>2       | Eth Chaild | Changin around           |
| Chassis ground        | Eth_Sheild     | 1   | 4              | Eth_Sheild | Chassis ground           |
| Chassis ground        | Eth_Sheild     | 3   |                | Eth_Sheild | Chassis ground           |
|                       | NC NC          | 5   | 6              | NC NC      |                          |
|                       | NC             | 7   | 8              | NC         |                          |
|                       | Port1_T0+      | 9   | 10             | Port2_T0+  |                          |
|                       | Port1_T0-      | 11  | 12             | Port2_T0-  |                          |
|                       | NC             | 13  | 14             | NC         |                          |
|                       | Port1_T1+      | 15  | 16             | Port2_T1+  |                          |
|                       | Port1_T1-      | 17  | 18             | Port2_T1-  |                          |
| Gigabit Ethernet port | NC             | 19  | 20             | NC         | Gigabit Ethernet port    |
|                       | Port1_T2+      | 21  | 22             | Port2_T2+  |                          |
|                       | Port1_T2-      | 23  | 24             | Port2_T2-  |                          |
|                       | NC             | 25  | 26             | NC         |                          |
|                       | Port1_T3+      | 27  | 28             | Port2_T3+  |                          |
|                       | Port1_T3-      | 29  | 30             | Port2_T3-  |                          |
|                       | NC NC          | 31  | 32             | NC         | / \ /                    |
|                       | NC             | 33  | 34             | NC NC      |                          |
|                       |                | 33  | J <del>4</del> |            | Running indicator        |
| Test button           | TEST_INPUT_KEY | 35  | 36             | STA_LED-   | (active low)             |
|                       | GND            | 37  | 38             | GND        |                          |
|                       | A              | 39  | 40             | DCLK       | Shift clock output 1     |
|                       | В              | 41  | 42             | DCLK_2     | Shift clock output 2     |
| Line decoding signal  | С              | 43  | 44             | LAT        | Latch signal output      |
|                       | D              | 45  | 46             | CTRL       | Afterglow control signal |
|                       | E              | 47  | 48             | OE_RED     | Display enable           |
| Display enable        | OE_BLUE        | 49  | 50 🗷           | OE_GREEN   | Display enable           |
| Diopidy chasic        | GND            | 51  | 52             | GND        | Biopiay onabio           |
| 1                     | G1             | 53  | 54             | R1         | 1                        |
| 1                     | _              |     |                |            | /                        |
| /                     | R2             | 55  | 56             | B1         | /                        |
| 1                     | B2             | 57  | 58             | G2         | /                        |
| 1                     | G3             | 59  | 60             | R3         | /                        |
| 1                     | R4             | 61  | 62             | B3         | /                        |
| 1                     | B4             | 63  | 64             | G4         | 1                        |
|                       | GND            | 65  | 66             | GND        |                          |
| /                     | G5             | 67  | 68             | R5         | /                        |
| 1                     | R6             | 69  | 70             | B5         | /                        |
| /                     | B6             | 71  | 72             | G6         | /                        |
| I                     | G7             | 73  | 74             | R7         | /                        |
| 1                     | R8             | 75  | 76             | B7         | /                        |
| 1                     | B8             | 77  | 78             | G8         | /                        |
|                       | GND            | 79  | 80             | GND        |                          |
|                       | G9             | 81  | 82             | R9         | /                        |
|                       | R10            | 83  | 84             | B9         | /                        |
| 1                     | B10            | 85  | 86             | G10        | /                        |
| 1                     | G11            | 87  | 88             | R11        | 1                        |
| 1                     | R12            | 89  | 90             | B11        | 1                        |
| <del>'</del>          | B12            | 91  | 90             | G12        | 1                        |
| 1                     |                |     |                |            | l l                      |
| ,                     | GND            | 93  | 94             | GND        | ,                        |
| 1                     | G13            | 95  | 96             | R13        | 1                        |
| 1                     | R14            | 97  | 98             | B13        |                          |
| 1                     | B14            | 99  | 100            | G14        | /                        |
| 1                     | G15            | 101 | 102            | R15        | /                        |
| 1                     | R16            | 103 | 104            | B15        | 1                        |
| 1                     | B16            | 105 | 106            | G16        | 1                        |
|                       | GND            | 107 | 108            | GND        |                          |
|                       | NC             | 109 | 110            | NC         |                          |
|                       | NC             | 111 | 112            | NC         |                          |
|                       | NC             | 113 | 114            | NC         |                          |
|                       | NC             | 115 | 116            | NC         |                          |
|                       | GND            | 117 | 118            | GND        |                          |
| L                     | OND            |     | 110            | S.E.D      | l                        |

| JH2 |     |     |     |     |  |
|-----|-----|-----|-----|-----|--|
|     | GND | 119 | 120 | GND |  |

### Pins for 64 Groups of Serial Data



|                        | JH1         |    |    |        |   |  |
|------------------------|-------------|----|----|--------|---|--|
|                        | GND         | 1  | 2  | GND    |   |  |
| LCD CS signal          | EXT_LCD_CS  | 3  | 4  | NC     |   |  |
| LCD RS signal          | EXT_LCD_RS  | 5  | 6  | NC     |   |  |
| LCD clock signal       | EXT_LCD_SCL | 7  | 8  | NC     |   |  |
| LCD data signal        | EXT_LCD_SDA | 9  | 10 | NC     |   |  |
| LCD backlight signal 1 | EXT_LCD_BL0 | 11 | 12 | NC     |   |  |
| LCD backlight signal 2 | EXT_LCD_BL1 | 13 | 14 | NC     |   |  |
| LCD control button     | EXT_KEY     | 15 | 16 | NC     |   |  |
| 1                      | RFU1        | 17 | 18 | NC     |   |  |
| 1                      | RFU2        | 19 | 20 | NC     |   |  |
|                        | GND         | 21 | 22 | NC     |   |  |
|                        | NC          | 23 | 24 | NC     |   |  |
|                        | GND         | 25 | 26 | GND    |   |  |
| 1                      | Data50      | 27 | 28 | Data49 | / |  |
| 1                      | Data52      | 29 | 30 | Data51 | 1 |  |

| JH1 |           |     |     |           |   |
|-----|-----------|-----|-----|-----------|---|
| 1   | Data54    | 31  | 32  | Data53    | / |
| 1   | Data56    | 33  | 34  | Data55    | / |
| 1   | Data58    | 35  | 36  | Data57    | / |
| 1   | Data60    | 37  | 38  | Data59    | / |
|     | GND       | 39  | 40  | GND       |   |
| 1   | Data62    | 41  | 42  | Data61    | / |
| 1   | Data64    | 43  | 44  | Data63    | 1 |
|     | NC        | 45  | 46  | NC        |   |
|     | NC        | 47  | 48  | NC        |   |
|     | NC        | 49  | 50  | NC        |   |
|     | NC        | 51  | 52  | NC        |   |
|     | GND       | 53  | 54  | GND       |   |
|     | NC        | 55  | 56  | NC        |   |
|     | NC        | 57  | 58  | NC        |   |
|     | NC        | 59  | 60  | NC        |   |
|     | NC        | 61  | 62  | NC        |   |
|     | NC        | 63  | 64  | NC        |   |
|     | NC        | 65  | 66  | NC        |   |
|     | GND       | 67  | 68  | GND       |   |
|     | NC        | 69  | 70  | NC        |   |
|     | NC        | 71  | 72  | NC        | ) |
|     | NC        | 73  | 74  | NC        |   |
|     | NC        | 75  | 76  | NC        |   |
|     | NC        | 77  | 78  | NC        |   |
|     | NC        | 79  | 80  | NC        |   |
|     | GND       | 81  | 82  | GND       |   |
| 1   | RFU4      | 83  | 84  | RFU3      | / |
| 1   | RFU6      | 85  | 86  | RFU5      | / |
| 1   | RFU8      | 87  | 88  | RFU7      | / |
| 1   | RFU10     | 89  | 90  | RFU9      | / |
| 1   | RFU12     | 91  | 92  | RFU11     | / |
| 1   | RFU14     | 93  | 94  | RFU13     | / |
| ,   | GND       | 95  | 96  | GND       | , |
| 1   | RFU16     | 97  | 98  | RFU15     | / |
| 1   | RFU18     | 99  | 100 | RFU17     | / |
|     | NC<br>NC  | 101 | 102 | NC<br>NO  |   |
|     | NC<br>NC  | 103 | 104 | NC<br>NC  |   |
|     | NC<br>NC  | 105 | 106 | NC<br>NC  |   |
|     | NC<br>CND | 107 | 108 | NC<br>CND |   |
| ~   | GND       | 109 | 110 | GND       |   |
|     | GND       | 111 | 112 | GND       |   |
|     | NC<br>VCC | 113 | 114 | NC<br>VCC |   |
|     |           | 115 | 116 |           |   |
|     | VCC       | 117 | 118 | VCC       |   |
|     | VCC       | 119 | 120 | VCC       |   |

|                       | JH2        |    |    |            |                         |
|-----------------------|------------|----|----|------------|-------------------------|
| Chassis ground        | Eth_Sheild | 1  | 2  | Eth_Sheild | Chassis ground          |
| Chassis ground        | Eth_Sheild | 3  | 4  | Eth_Sheild | Chassis ground          |
|                       | NC         | 5  | 6  | NC         |                         |
|                       | NC         | 7  | 8  | NC         |                         |
|                       | Port1_T0+  | 9  | 10 | Port2_T0+  | Circle it Ethornot nort |
|                       | Port1_T0-  | 11 | 12 | Port2_T0-  |                         |
|                       | NC         | 13 | 14 | NC         |                         |
| Cigobit Ethornot port | Port1_T1+  | 15 | 16 | Port2_T1+  |                         |
| Gigabit Ethernet port | Port1_T1-  | 17 | 18 | Port2_T1-  | Gigabit Ethernet port   |
|                       | NC         | 19 | 20 | NC         |                         |
|                       | Port1_T2+  | 21 | 22 | Port2_T2+  |                         |
|                       | Port1_T2-  | 23 | 24 | Port2_T2-  |                         |

| JH2                  |                |          |     |           |                                |
|----------------------|----------------|----------|-----|-----------|--------------------------------|
|                      | NC             | 25       | 26  | NC        |                                |
|                      | Port1_T3+      | 27       | 28  | Port2_T3+ |                                |
|                      | Port1_T3-      | 29       | 30  | Port2_T3- |                                |
|                      | NC             | 31       | 32  | NC        |                                |
|                      | NC             | 33       | 34  | NC        |                                |
| Test button          | TEST_INPUT_KEY | 35       | 36  | STA_LED-  | Running indicator (active low) |
|                      | GND            | 37       | 38  | GND       | ,                              |
|                      | A              | 39       | 40  | DCLK      | Shift clock output 1           |
|                      | В              | 41       | 42  | DCLK_2    | Shift clock output 2           |
| Line decoding signal | С              | 43       | 44  | LAT       | Latch signal output            |
|                      | D              | 45       | 46  | CTRL      | Afterglow control signal       |
|                      | E              | 47       | 48  | OE_RED    |                                |
| Display enable       | OE_BLUE        | 49       | 50  | OE_GREEN  | Display enable                 |
| -1 -7                | GND            | 51       | 52  | GND       |                                |
| 1                    | Data2          | 53       | 54  | Data1     | -/-                            |
| 1                    | Data4          | 55       | 56  | Data3     | 1                              |
| 1                    | Data6          | 57       | 58  | Data5     | 7                              |
| 1                    | Data8          | 59       | 60  | Data7     |                                |
| 1                    | Data10         | 61       | 62  | Data9     | /                              |
| 1                    | Data12         | 63       | 64  | Data11    | ,                              |
| ,                    | GND            | 65       | 66  | GND       | ,                              |
| 1                    | Data14         | 67       | 68  | Data13    | /                              |
| 1                    | Data14  Data16 | 69       | 70  | Data15    | /                              |
| 1                    | Data18         | 71       | 72  | Data 17   | /                              |
| 1                    | Data 20        | 73       | 74  | Data19    | /                              |
| '                    | Data20         | 75       | 76  | Data 19   | /                              |
| ,                    | Data24         | 77       | 78  | Data23    | /                              |
| ,                    | GND            | 79       | 80  | GND       | ,                              |
| ,                    | Data26         | 81       | 82  | Data25    | /                              |
|                      | *****          |          |     |           | /                              |
|                      | Data28         | 83<br>85 | 84  | Data27    | /                              |
|                      | Data30         |          | 86  | Data29    | /                              |
| 1                    | Data32         | 87       | 88  | Data31    | 1                              |
| 1                    | Data34         | 89       | 90  | Data33    | 1                              |
| /                    | Data36         | 91       | 92  | Data35    | 1                              |
| ,                    | GND            | 93       | 94  | GND       | 1                              |
| /                    | Data38         | 95       | 96  | Data37    | /                              |
|                      | Data40         | 97       | 98  | Data39    | /                              |
| 1                    | Data42         | 99       | 100 | Data41    | /                              |
| 1                    | Data44         | 101      | 102 | Data43    | /                              |
|                      | Data46         | 103      | 104 | Data45    | /                              |
|                      | Data48         | 105      | 106 | Data47    | /                              |
|                      | GND            | 107      | 108 | GND       |                                |
|                      | NC             | 109      | 110 | NC        |                                |
|                      | NC             | 111      | 112 | NC        |                                |
|                      | NC             | 113      | 114 | NC        |                                |
| *                    | NC             | 115      | 116 | NC        |                                |
|                      | GND            | 117      | 118 | GND       |                                |
|                      | GND            | 119      | 120 | GND       |                                |



The recommended VCC power input is 5.0 V.

OE\_RED, OE\_GREEN and OE\_BLUE are display enable pins. When RGB are not controlled separately, use OE\_RED. When the PWM chip is used, those pins are used as GCLK pins.

In the mode of 128 groups of serial data, Data65–Data128 use the data of Data1–Data64, respectively.

### **Reference Design for Extended Functions**

Pins for Extended Functions



|       | Pins for Extended Functions     |                                 |                                      |  |  |  |  |
|-------|---------------------------------|---------------------------------|--------------------------------------|--|--|--|--|
| Pin   | Recommended<br>Smart Module Pin | Recommended<br>Module Flash Pin | Description                          |  |  |  |  |
| RFU1  | Reserved                        | Reserved                        | A reserved pin for connection to MCU |  |  |  |  |
| RFU2  | Reserved                        | Reserved                        | A reserved pin for connection to MCU |  |  |  |  |
| RFU3  | HUB_CODE0                       | HUB_CODE0                       | Flash control pin 1                  |  |  |  |  |
| RFU4  | HUB_SPI_CLK                     | HUB_SPI_CLK                     | Clock signal of serial pin           |  |  |  |  |
| RFU5  | HUB_CODE1                       | HUB_CODE1                       | Flash control pin 2                  |  |  |  |  |
| RFU6  | HUB_SPI_CS                      | HUB_SPI_CS                      | CS signal of serial pin              |  |  |  |  |
| RFU7  | HUB_CODE2                       | HUB_CODE2                       | Flash control pin 3                  |  |  |  |  |
| RFU8  | /                               | HUB_SPI_MOSI                    | Module Flash data storage input      |  |  |  |  |
| KFU6  | HUB_UART_TX                     | /                               | Smart module TX signal               |  |  |  |  |
| RFU9  | HUB_CODE3                       | HUB_CODE3                       | Flash control pin 4                  |  |  |  |  |
| RFU10 | /                               | HUB_SPI_MISO                    | Module Flash data storage output     |  |  |  |  |
| KFUIU | HUB_UART_RX                     | /                               | Smart module RX signal               |  |  |  |  |
| RFU11 | HUB_H164_CSD                    | HUB_H164_CSD                    | 74HC164 data signal                  |  |  |  |  |
| RFU12 | /                               | /                               | 1                                    |  |  |  |  |
| RFU13 | HUB_H164_CLK                    | HUB_H164_CLK                    | 74HC164 clock signal                 |  |  |  |  |
| RFU14 | POWER_STA1                      | POWER_STA1                      | Dual power supply detection signal 1 |  |  |  |  |
| RFU15 | MS_DATA                         | MS_DATA                         | Dual card backup connection signal   |  |  |  |  |
| RFU16 | POWER_STA2                      | POWER_STA2                      | Dual power supply detection signal 2 |  |  |  |  |
| RFU17 | MS_ID                           | MS_ID                           | Dual card backup identifier signal   |  |  |  |  |
| RFU18 | HUB_CODE4                       | HUB_CODE4                       | Flash control pin 5                  |  |  |  |  |



The RFU8 and RFU10 are signal multiplex extension pins. Only one pin from either the Recommended Smart Module Pin or the Recommended Module Flash Pin can be selected at the same time.

# **Specifications**

| Maximum<br>Loading Capacity | 512×512 pixels (PWM IC) |   |  |  |  |
|-----------------------------|-------------------------|---|--|--|--|
| Electrical<br>Parameters    | Input voltage           | DC 3.3 V to 5.5 V   |  |  |  |
| Farameters                  | Rated current           | 0.6 A   |  |  |  |
|                             | Rated power consumption | 2.5 W   |  |  |  |
| Operating<br>Environment    | Temperature             | -20°C to +70°C  |  |  |  |
| Humidity                    |                         | 10% RH to 90% RH, non-condensing  |  |  |  |
| Storage                     | Temperature             | −25°C to +125°C   |  |  |  |
| Environment                 | Humidity                | 0% RH to 95% RH, non-condensing   |  |  |  |
| Physical                    | Dimensions              | 80.0 mm × 45.0 mm × 9.1 mm  |  |  |  |
| Specifications              | Net weight              | 22.3 g  |  |  |  |
| Packing<br>Information      | Packing specifications  | An antistatic bag and anti-collision foam are provided for each receiving card. Each packing box contains 40 receiving cards. |  |  |  |
|                             | Packing box dimensions  | 378.0 mm × 190.0 mm × 120.0 mm  |  |  |  |
| Certifications              | RoHS, EMC Class B       |   |  |  |  |

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